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APPLICANTS

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** CONTINUING DATA *****

None

** FOREIGN APPLICATIONS *****

JAPAN 2003-034859 02/13/2003

de Yes

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

04/08/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				

Verified and Acknowledged

*Thellak Jeth**P*

Examiner's Signature

Initials

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TITLE

Error portion detecting method and layout method for semiconductor integrated circuit

FILING FEE RECEIVED 942	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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